

U.S. PTO  
10/061492  
JCS73 01/31/02

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10061492	FILING DATE 01/31/2002	CLASS 683	SUBCLASS 216	GAU 27	EXAMINER R. Culbert
**APPLICANTS: Buswell Shen; Rivas Rio; Khavari Mehrigan; Templin Paul; MacKenzie Mark, Jنسن Conrad; 216 27 1763 Culbert					
See updated Bib Sheet					
**CONTINUING DATA VERIFIED: M					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners initials M		□ yes <input checked="" type="checkbox"/> no □ yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO 10011880-1	
TITLE : Methods and systems for forming slots in a semiconductor substrate U.S.DEP.T. OF COMM./PAT. & TM-PTO-438L(Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.		Figs.Drwg.
		Print Fig.		
		Primary Examiner		
		PREPARED FOR ISSUE		
		Application Examiner		
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.				
<input type="checkbox"/> TERMINAL DISCLAIMER				

BEST AVAILABLE COPY

FILED WITH:

DISK (CRF)

CD-ROM  
(Attached in pocket on right inside flap)